

Notice of References Cited

Application/Control No.

09/981,291

Applicant(s)/Patent Under
Reexamination
GOODWIN ET AL.

Examiner

Wei Zhen

Art Unit

2191

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Bhattacharyya et al. Automatic Test Generation for Micro-architectural Verification of Configurable Microprocessor Cores with User Extensions.
	V	Pendleton et al. Fast Prolog with an Extended General Purpose Architecture. IEEE. 1990. pp. 282-291.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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